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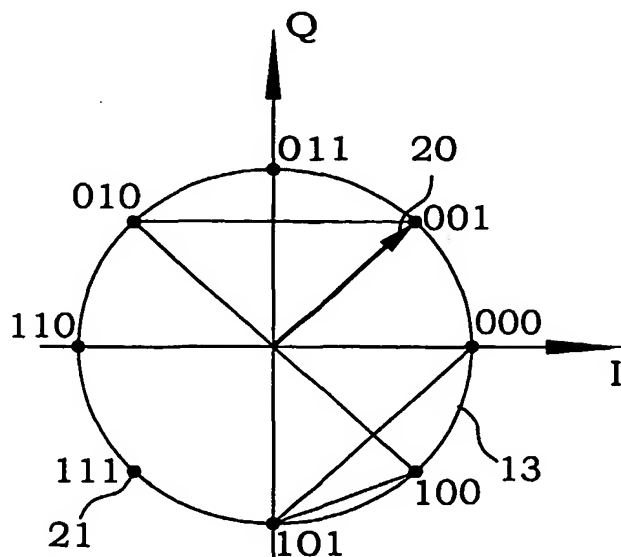
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(54) Title: MULTIMODULATION TRANSMITTER



(57) Abstract: The present invention discloses a double TRU (Transceiver Unit) (45). The output signals from the power amplifiers (64, 84) are combined to one common output signal provided to an antenna arrangement (91). A DSP (Digital Signal Processor) (52, 72) of each TRU (50, 70) comprises means for a constant-envelope modulation scheme (54, 74) and a non-constant envelope scheme (53, 73). The DSP:s (52, 72) select the modulation scheme according to modulation information (49, 69). In such a way, a switching between different modulation schemes can be performed even on a time-slot basis. For non-constant-envelope modulation, the modulated signal is separated into two component signals. Each TRU (50, 70) takes care of the amplification of one component. A phase compensation of at least one of the TRU:s (50, 70) is performed in order to correct for different paths of phases of the power amplifiers (64, 84). The non-constant envelope modulated signal can also be a multi-carrier signal, e.g. of two or more constant-envelope signals. Also a TCC (Transmitter Coherent Combining) operation is achievable.

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MULTIMODULATION TRANSMITTER

TECHNICAL FIELD

The present invention relates in general to wireless communication and in particular to wireless communication transmitter systems.

BACKGROUND

In conventional GSM (Global System for Mobile Communication), a modulation scheme according to GMSK (Gaussian Minimum-Shift Keying) is implemented. GMSK is a constant envelope modulation scheme, where a phase shift is differentially dependent on the bit sequence. The GMSK modulation has been chosen as a compromise between fairly high spectrum efficiency and reasonable demodulation complexity.

EDGE (Enhanced Data for Global Evolution) is a high-speed mobile data standard, intended to enable second-generation GSM and TDMA (Time Division Multiple Access) networks to transmit data up to 384 kbps. EDGE provides the speed enhancement by changing the type of modulation used and making better use of the carrier currently used. It enables a greater data transmission speed to be achieved in good conditions, in particular near the base stations by implementing 8PSK (Eight-Phase-Shift Keying) modulation. The 8PSK modulation scheme is a high transmission modulation based on phase shift coding. The modulation is of a non-constant envelope type. EDGE can co-exist with the existing GSM traffic, switching to EDGE mode when appropriate.

When upgrading a base station to handle EDGE, the transmitter system has to be modified. A transmitter used for standard GSM purposes is designed for supporting GMSK, which means that the power amplifier that are used typically are more or less non-linear. When implementing 8PSK, the envelope may vary in a pre-defined way over time, and non-linear amplification can

not be accepted. Thus, in a general case, a new parallel transmitter arrangement has to be provided. Since the transmitter devices are costly, parallel transmitter arrangements, which are only used one at a time, means a poor utilization of installed equipment. Furthermore, highly linear power amplifier elements or arrangements are very expensive and there is a request to avoid solutions using such elements.

In "Increasing the talk-time of mobile radios with efficient linear transmitter architectures" by S. Mann, M. Beach, P. Warr and J. McGeehan, *Electronics & Communication Engineering Journal*, April 2001, Vol. 13, No. 2, pp. 65-76, the relationship between linearizing methods for power amplification in radio transmitters and efficiency is discussed. LINC (Linear Nonlinear Component), known in prior art e.g. by US 5,990,734, is one of the investigated schemes, where one non-constant envelope signal is divided into two constant envelope signals, which subsequently can be amplified by non-linear amplifiers. However, since such a method requires two non-linear amplifiers, this is not a particularly efficient approach for systems also handling constant envelope signals.

SUMMARY

An object of the present invention is to provide for using one and the same transmitter system for constant-envelope as well as non-constant envelope modulation schemes. Another object is to provide a transmitter system for non-constant envelope modulation schemes based on non-linear power amplifier elements. A further object of the present invention is to provide the possibility for fast switching between different modulation schemes.

The above objects are achieved by methods and devices according to the enclosed claims. In general words, a double TRU (Transceiver Unit) is used. The output signals from the power amplifiers are combined to one common output signal provided to an antenna arrangement. A DSP (Digital Signal Processor) of each TRU comprises means for a constant-envelope modulation

scheme and a non-constant envelope scheme. The DSP:s select the modulation scheme according to modulation information provided together with the input digital signal. In such a way, a switching between different modulation schemes can be performed even on a time-slot basis.

In case of a non-constant-envelope modulation, the DSP divides the modulated signal into two component signals. Each TRU takes care of the amplification of one component, and the components are eventually combined before provided to the antenna arrangement. A phase compensation of at least one of the TRU:s is performed in order to correct for different paths or phase positions of the power amplifiers. The non-constant envelope modulated signal can also be a multi-carrier signal, e.g. of two or more constant-envelope signals.

For normal constant-envelope modulation, the two TRU:s are operating independently of each other, and the two output signals are combined to a double-carrier signal.

The arrangement can also be operated according to TCC (Transmitter Coherent Combining) of constant-envelope modulated signals, where both TRU:s are provided with the same digital signal. The two amplified output signals are combined to create an output signal of double the amplitude. Also here, phase compensation is necessary.

The phase compensation is preferably determined by monitoring the output power or monitoring the power in the load of the hybrid and comparing with expected output power. In one embodiment, a calibration of the phase compensation is performed during TCC bursts, and utilized during non-constant envelope modulation. Other embodiments utilize constant amplitude portions of non-constant envelope time slots for performing phase compensation calibration. One may then make use also of power measurements of the output signals from each power amplifier. The phase

compensation calibration can also be performed during well-characterized training sequences within the time slots.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention, together with further objects and advantages thereof, may best be understood by making reference to the following description taken together with the accompanying drawings, in which:

FIG. 1 is a vector diagram illustrating a constant envelope signal;

FIG. 2 is a vector diagram illustrating a non-constant envelope signal;

FIG. 3 is a vector diagram illustrating principles of separating an arbitrary signal into two constant envelope signals;

FIG. 4 is a vector diagram illustrating the principles of transmitter coherent combining;

FIG. 5 is a vector diagram illustrating the effect of phase errors in transmitter coherent combining;

FIG. 6 is a block diagram illustrating an embodiment of a double transmitter unit according to the present invention;

FIG. 7 is a flow diagram illustrating an embodiment of a method for providing two constant envelope modulated signals according to the present invention;

FIG. 8 is a flow diagram illustrating an embodiment of a method for providing a non-constant envelope modulated signal according to the present invention;

FIG. 9 is a flow diagram illustrating an embodiment of a method providing transmitter coherent combining according to the present invention;

FIG. 10 is block diagram illustrating another embodiment of a double transmitter unit according to the present invention;

FIG. 11 is a flow diagram illustrating another embodiment of a method for providing a non-constant envelope modulated signal according to the present invention;

FIG. 12 is a flow diagram illustrating another embodiment of a method providing transmitter coherent combining according to the present invention;

FIG. 13 is a diagram illustrating a power versus time mask for an 8PSK modulated normal burst;

FIG. 14 is a block diagram of a part of yet another embodiment of a double transmitter unit according to the present invention;

FIG. 15 illustrates a time slot used in GMSK or 8PSK modulation;

FIG. 16 is a block diagram illustrating an alternative phase shifter solution applicable to the present invention;

FIG. 17 is a diagram illustrating the principles of the phase shifter of Fig. 16;

FIG. 18 is a block diagram illustrating a part supporting frequency hopping of an embodiment of the present invention;

FIG. 19 is an illustration of a storage of phase shifts usable together with the present invention; and

FIG. 20 is a block diagram of one transmitter unit supporting double carrier signals of an embodiment of a double transmitter unit according to the present invention.

DETAILED DESCRIPTION

A signal modulated according to the GMSK modulation scheme can be visualized in the complex signal plane as illustrated in Fig. 1. The coordinate system is here supposed to rotate synchronously with the base frequency of the carrier, and only the phase differences will therefore appear in the diagram. A modulated signal is thereby represented by a vector 10. In GMSK, the phase shift is adjusted according to three successive bits in the digital input signal. Generally speaking, the phase is smoothly shifted $\pi/2$ if the three successive digits are the same. This means that the vector 10 shifts counterclockwise according to arrow 11 if the successive digits are the same and clockwise according to arrow 12 if they are different. All the time, the vector 10 maintain its magnitude, i.e. the end of the vector 10 always travels on a circle 13 in the I-Q-space. The modulation scheme is therefore said to be a constant-envelope scheme. It is relatively easy to amplify a constant-envelope signal, since also non-linear power amplifiers may be used. Since

the signal always has one and the same envelope, the gain is always the same, regardless of the linearity of the amplifier. Simpler power amplifier solutions can thereby be used.

A signal modulated according to the 8PSK modulation scheme can be also be visualized in the complex signal plane as illustrated in Fig. 2. The coordinate system is also here supposed to rotate synchronously with the base frequency of the carrier, and only the phase differences will therefore appear in the diagram. A modulated signal is thereby represented by a vector 20. In 8PSK, the phase shift is a coding of a triplet of binary digits. A certain phase shift corresponding to a specific set of digits, as indicated by points 21 in the figure. Depending on the scheme, the assignment may vary, and there might also be an offset phase shift present, which removes the points from the axes. However, the example in Fig. 2 illustrates well the principle. When changing from one triplet to the next, the vector moves from one point 21 to another and passes thereby through the interior of the circle 13. The modulation scheme is therefore said to be a non-constant-envelope scheme. When amplifying an 8PSK signal, the amplifier arrangement has to have relatively linear characteristics, since the signal will change its magnitude. Highly linear amplifiers are expensive and simpler solutions are desired.

One possible approach to provide a linear amplification is to decompose or separate the signal into two component signals, amplify these component signals and combine the amplified component signals again. If one keeps the component magnitudes constant, even non-linear amplifiers can be used. This principle, LINC (Linear Nonlinear Component) amplifier, is known in prior art, e.g. by US 5,990,734. One thereby trades one linear amplifier for two non-linear ones, plus a separator and combiner. Fig. 3 illustrates the principles. A signal to be amplified is represented by a vector 30. The vector 30 has a varying size S and phase shift ϕ . The vector 30 is separated into two component vectors 31, 32. In a first embodiment, the amplitude A of the components is the same and follows the circle 13. This amplitude has to be at least half of the maximum amplitude of the vector 30. Also the phase

difference α to the vector 30 is the same, however, directed in opposite directions. According to basic geometrical considerations, the component phase shifts θ_1 and θ_2 are specified by:

$$\theta_1 = \varphi - \arccos(S/2A)$$

$$\theta_2 = \varphi + \arccos(S/2A).$$

As anyone skilled in the art understand, it is by this possible to express any arbitrary vector of length $\leq 2A$ by two component vectors of length A . The component signals can then be amplified according to principles of amplifying constant-envelope signals.

In a more general case, the amplitudes of the components may be different and may also vary depending on the size and phase of the vector 30. Such applications will be discussed more in detail below.

A special case of combining two component signals into one final output signal is in case two component signals always with the same phase are combined. This can be used in cases where a high amplification is desired, and where it is difficult to achieve by only one amplifier. As shown in Fig. 4, two components 41, 42 (seen as one vector in the figure) of the same phase can then be amplified separately and combined into an output signal 43. This is the basic idea of TCC (Transmitter Coherent Combining).

A practical problem in combining two separate signals into one output signal is that the paths through the amplifiers typically involves some path difference or that the devices are locked in different phase positions, which will be noticed as a small phase shift between the two component signals. Such a situation is illustrated in Fig. 5. A signal 30 is separated into two components 31, 32. During amplification, the first one of the components is shifted $\Delta\theta$ compared to the second one. This phase shifted components is illustrated by a broken arrow 34. The actual composed output signal 33 will then be changed both in phase and amplitude. One solution is to measure

the phase shift difference carefully and compensate for it by introducing phase altering means in one of the paths. However, such a phase shift may also be slowly varying with time, and in such cases, an adaptive phase compensating arrangement has to be introduced. A preferred embodiment of such an arrangement will be described further below.

An embodiment of a double transmitter unit arrangement 45 according to the present invention is illustrated in Fig. 6. A first modulation unit 50 has an input 51 for receiving a digital signal to be transmitted. The input 51 is connected to a DSP (digital signal processor) 52. The DSP 52 comprises modulation means; a 8PSK modulator 53 and a GMSK modulator 54. The DSP 52 also comprises a control input 49 for receiving modulation information, and a selector 55. The selector 55 selects one of the modulators 53, 54 according to the modulation information received by the control input 49. The digital signal received by the input 51 is thereby provided to one of the modulators 53, 54. The different means in the DSP 52 are implemented as software.

The GMSK modulator 54 modulates the input digital signal according to the GMSK scheme. The modulated signal is in this embodiment provided in a real I and an imaginary part Q at two outputs, connected to an analogue signal generator 56. In this embodiment, the analogue signal generator 56 comprises essentially a quadrature modulator 57. The analogue signal generator 56 comprises in turn two DAC's (Digital-to-Analogue Converters) 58, 59 converting the I and Q signals, respectively, into analogue voltages. The analogue voltages are modulated in a mixer 60 with the carrier frequency, provided by a frequency generator 61, and combined. A phase shifter 62 shifts the frequency signal to the Q component by 90 degrees. The output from the analogue signal generator 56 is thus an analogue voltage signal being modulated, in this case according to the GMSK scheme.

A phase shifter 63 is in this embodiment connected between the modulators 53, 54 and the quadrature modulator 57. The function of this unit will be

described more in detail further below. The analogue signal from the analogue signal generator 56 is provided to a power amplifier 64 for amplification. In the present embodiment, the power amplifier 64 is a non-linear amplifier. The amplified signal from the output of the power amplifier 64 is provided to an input of a hybrid combiner device 90.

The output of the 8PSK modulator 53 is provided to a separator 65. The separator separates the signal provided from the 8PSK modulator into two components, whereby the input signal is the vector sum of the two components. A first one of the components is provided to the input of the analogue signal generator 56, in the form of an I and a Q signal. In the present embodiment, the second component is terminated. The first component is processed in the analogue signal generator 56 in the same manner as described above.

A second modulator unit 70 is very similar to the first modulation unit 50. It has an input 71 for a digital signal and a control input 69. A DSP 72 comprises analogously a 8PSK modulator 73 and a GMSK modulator 74, and a selector 75 selecting which of the modulator that is going to be used.

The GMSK modulator 74 is similarly connected to an analogue signal generator 76, having a quadrature modulator 77. However, no phase shifter is present. The quadrature modulator 77 is of the same structure as the one in the first modulation unit 50.

The analogue signal from the analogue signal generator 76 is provided to a power amplifier 84 for amplification. In the present embodiment, the power amplifier 84 is a non-linear amplifier of the same type as the power amplifier 64. The amplified signal from the output of the power amplifier 84 is provided to a second input of the hybrid combiner device 90.

The output of the 8PSK modulator 73 is provided to a separator 85, having the same function as the separator 65. The separator 85 separates the signal

provided from the 8PSK modulator into two components, whereby the input signal is the vector sum of the two components. In the present embodiment, the first component is terminated. The second component is instead provided to the input of the analogue signal generator 76, in the form of an I and a Q signal. The second component is processed in the analogue signal generator 76 in the same manner as described above.

The hybrid combiner device 90 combines the two signals provided by the outputs of the two power amplifiers 64, 84 into a transmitter signal, that is provided to a transmitter device 91. The input power supplied by the power amplifiers 64, 84 is at least to a part provided as a transmitter signal power. However, any remaining power will be dissipated by a hybrid load 92.

In the present embodiment, the power dissipated over the hybrid load 92 is measured by a power meter 93. The output of the power meter 93 is connected to the phase shifter 63 via an ADC (Analogue-to-Digital Converter). The value of the hybrid load power is provided to a phase controller 94, which calculates any phase shift between the amplified signals provided to the hybrid. The phase shifter 63 further comprises a complex multiplier 95, providing a digital phase shift angle $e^{i\Delta\theta}$ to the I and Q signals respectively. This phase shift is thus in a complex manner incorporated in the I and Q signals that are entering into the analogue signal generator 56.

By this double transmitter unit arrangement 45, a number of different modulation techniques can be employed. By accompanying the digital signals with associated modulation information, the switching between different modulation schemes can be performed very swiftly, even on a time slot basis. Such an arrangement thus allows the transmitter arrangement 45 to allow for mixing e.g. GMSK bursts with 8PSK traffic on a time slot basis.

Some examples of different operation modes of the double transmitter unit 45 are given here below. Assume normal GMSK traffic. The double transmitter unit 45 then operates as two independent transmitter paths,

having one carrier each. A digital signal of a first carrier is provided to the first modulation unit 50, while a digital signal of a second carrier is provided to the second modulation unit 70. The modulation information instructs both DSP's 52, 72 to select a GMSK modulation. The two carrier signals are combined in the hybrid combiner 90 into a common signal, provided to the transmitter. The phase shifter arrangement is in this case not used.

In case a GMSK signal with a high output power is desired, a TCC arrangement can be achieved. In such a case, the same digital signal is provided to both modulation units 50, 70 together with a request for GMSK modulation. Both transmitter units are processing the same signal and the combined signal at the hybrid combiner 90 output is ideally of double the output power. In comparison with combining two different carriers, the TCC carrier is provided with a power four times higher. This is due to the fact that half the power dissipates in the load when combining two different carriers, while coherent combining removes all power from the load. However, as discussed above, any phase shifts caused by path differences in the two branches may deteriorate the total signal. In this TCC arrangement, the phase shifter 63 comes into use. In this embodiment, the power over the hybrid load 92 is measured. If the amplifier branches are perfectly aligned in phase, all power will be distributed to the transmitter device 91, which means that no power will be dissipated through the hybrid load. By adjusting the phase of the signal in one of the paths, the hybrid load power can be minimized, which indicates an alignment in phase of the two components.

A third operational mode is when a 8PSK signal is to be transmitted. Also in this case, both inputs 51, 71 are provided with the same digital signal. This signal will be modulated according to the 8PSK scheme since the selectors 55, 75 selects the 8PSK modulator 53, 73. The separator 65 in the DSP 52 of the first modulation unit 50 provides a first component signal to the analogue signal generator 56. The separator 85 in the DSP 72 of the second modulation unit 70 provides instead a second component signal to the

analogue signal generator 76. The vector sum of these two components equals the original 8PSK-modulated signal. Each of the components are amplified in a separate power amplifier 64, 84, and combined in the hybrid combined device 90 to form an amplified version of the original signal. The double transmitter unit arrangement 45 thus here operates at least partly in accordance with the LINC concept, providing one 8PSK carrier signal. Also here, phase shifts between the paths may appear. Different approaches for solving this problems are discussed further below.

In Fig. 7-9, the above operations are illustrated as flow diagrams. First, in Fig. 7, an embodiment of a method of providing two GMSK signals on one carrier each according to the present invention is illustrated. The procedure starts in step 100. In step 101, a first digital signal is provided to a first transmitter unit. This first digital signal is intended to be transmitted on a first carrier. In step 102, a second digital signal is provided to a second transmitter unit. This second digital signal is intended to be transmitted on a second carrier. In step 103, constant-envelope modulation information is provided to the first transmitter unit. In step 104, constant-envelope modulation information is provided to the second transmitter unit. A constant-envelope modulation scheme is selected and applied in the first transmitter unit according to the modulation information in step 105, and a constant-envelope modulation scheme is selected and applied in the second transmitter unit according to the modulation information in step 106. A first analogue signal corresponding to the first digital signal modulated according to the information is generated in step 110. A second analogue signal corresponding to the second digital signal modulated according to the information is generated in step 111. In step 112, the first analogue signal is amplified and in step 113, the second analogue signal is amplified. In step 114, the two amplified signals are combined to a two-carrier output signal to be transmitted. The procedure is ended in step 115.

Fig. 8 illustrates an embodiment of a method of providing a signal modulated according to a 8PSK modulation according to the present invention is

illustrated. The procedure starts in step 120. In step 121, a digital signal is provided to a first transmitter unit and the same digital signal is also provided to a second transmitter unit. In step 123, non-constant-envelope modulation information is provided to the first transmitter unit and to the second transmitter unit. A non-constant-envelope modulation scheme is selected and applied in the first transmitter unit according to the modulation information in step 125, and a non-constant-envelope modulation scheme is selected and applied in the second transmitter unit according to the modulation information in step 126. In step 127, the modulated signal in the first transmitter is separated into a first and a second component. In step 128 the modulated signal in the second transmitter is separated into the same first and second components. The first component is phase shifted in step 129 to compensate for differences in phase characteristics between the paths through amplifier stages of the first and second transmitter unit, respectively. A first analogue signal corresponding to the first phase-shifted component is generated in step 130. A second analogue signal corresponding to the second component is generated in step 131. In step 132, the first analogue signal is amplified and in step 133, the second analogue signal is amplified. In step 134, the two amplified signals are combined to a single-carrier output signal to be transmitted. The procedure is ended in step 135.

Fig. 9 illustrates the case of a TCC operation. The procedure starts in step 140. In step 141, a digital signal is provided to a first transmitter unit and the same digital signal is provided also to a second transmitter unit. This digital signal is intended to be transmitted with a double intensity. In step 143, constant-envelope modulation information is provided to the first transmitter unit and to the second transmitter unit. A constant-envelope modulation scheme is selected and applied in the first transmitter unit according to the modulation information in step 145, and a constant-envelope modulation scheme is selected and applied in the second transmitter unit according to the modulation information in step 146. The first modulated signal is phase shifted in step 149 to compensate for differences in phase characteristics between the paths through amplifier

stages of the first and second transmitter unit, respectively. A first analogue signal corresponding to the first digital signal modulated according to the information and phase-shifted is generated in step 150. A second analogue signal corresponding to the second digital signal modulated according to the information is generated in step 151. In step 152, the first analogue signal is amplified and in step 153, the second analogue signal is amplified. In step 154, the two amplified signals are combined to double the amplitude of a single-carrier output signal to be transmitted. The procedure is ended in step 155.

The three flow diagrams exhibit large resemblances. The changes in the different steps are of such a character, that it can be changed by e.g. software as a response on e.g. the modulation information given. Such information can be provided on a time-slot basis, i.e. the requested modulation can be changed from one time-slot to the next. This implies that also the different operational modes of the present invention preferably are interchangeable from one time-slot to the next.

Some modifications of the above embodiments are also interesting to be disclosed. In Fig. 10, another embodiment of a double transmitter arrangement according to the present invention is illustrated. Most parts are identical to the ones in the first illustrated embodiment, and will not be discussed again. However, some clear differences are present. First of all, it can be noted that in the previous embodiment, identical modulation and separation operations are performed in parallel in the first and second modulation units 50, 70. This can be avoided by the present design, in which the second modulation unit 70 does not explicitly have any separator. Instead, a connection 66 connects the output for the second component of the separator 65 in the first modulation unit 50 with the input of the analogue signal generator 76 of the second modulation unit 70. In this manner, the second modulation unit 70 can be made somewhat simpler and the computational effort during the operation is concentrated to the first modulation unit 50. A connection 67 also connects the output of the GMSK

modulator of the first modulation unit 50 with the input of the analogue signal generator 76 of the second modulation unit 70. This enables the corresponding simplification to be performed for the TCC operation.

Fig. 11 illustrates a flow diagram corresponding to the 8PSK operation with the embodiment of Fig. 10. Since the steps present are identical with some of the steps of the procedure of Fig. 8 they are not discussed again. Basically, the steps 126 and 128 are omitted and the steps 121 and 123 are changed into steps 122 and 124 respectively, in which only the first transmitter unit is involved. The second component used in step 131 is in this embodiment, however, provided from the first modulation unit.

Fig. 12 illustrates a flow diagram corresponding to the TCC operation with the embodiment of Fig. 10. Since the steps present are identical with some of the steps of the procedure of Fig. 9 they are not discussed again. Basically, the step 146 is omitted and the steps 141 and 143 are changed into steps 142 and 144 respectively, in which only the first transmitter unit is involved. The second modulated signal used in step 151 is in this embodiment, however, provided from the first modulation unit.

Now returning to Fig. 6. In this embodiment, the phase shifting of the signal provided to the first power amplifier 64 was based on a measurement of the power of the hybrid combiner load 92. Since there is a complementary relationship between the power to the transmitter device 91 and the load 92, either power can be measured and the other can be calculated. Measuring the load power is a relatively easy task, but of course, a direct measuring of the power supplied to the transmitter device is possible. The evaluation performed in the phase shifter 63 has of course to be changed accordingly.

The phase shifting during TCC operation is relatively straight-forward. The power dissipated in the load 92 is minimised, and the two signals are thereby phase-synchronized. However, in the case of 8PSK operation, the possible manners of performing the phase-shifting are less obvious. In a

system, where the flexibility of the present invention is fully used, the character of the transmitted signals varies. If TCC operation appears occasionally, the phase-shifting can be calibrated during such TCC time slots. The values of the optimum phase shift can then be stored, e.g. in the phase shifter 63, to be used e.g. during 8PSK operation.

The situation may, however, be somewhat more complex if the arrangement is designed also for frequency hopping. In Fig. 18, a part of a double transmitter arrangement according to the present invention is illustrated. The analogue signal generator 56 of the first modulation unit 50 is illustrated to have access to two different frequency generators 61A and 61B. A switch 68 connects one frequency generator at a time to the quadrature modulator 57. In the meantime, the other frequency generator is controlled to be tuned to the next frequency to use. When the frequency change is to be carried through, the switch 68 selects the next frequency generator. Each frequency used may influence the amplifier equipment to give different phase shifts. This means that the phase shift applied to the signal to be amplified in e.g. TCC or 8PSK mode has to be calibrated at that particular frequency. If the phase shifts are calibrated during TCC mode and stored to be used in 8PSK mode, there has to be one phase shift value for each frequency used by the arrangement. Also, the two frequency generators 61A and 61B may give rise to different phase shifts, whereby one calibrated phase shift for each combination of frequency generator and frequency is needed. A signal can be sent from the frequency generators 61A, 61B to the phase shifter 63 by a connection 86, for instructing the phase shifter which phase shift to apply.

A storage 87 of phase shifts, e.g. comprised in the phase shifter 63 can be configured as illustrated by Fig. 19. The storage 87 is here designed as a look-up table, with two input variables, identity of frequency generator used and the frequency of that frequency generator.

There are also alternative ways of obtaining calibrated phase shifts for the 8PSK operation. These are necessary in cases there are no or very few TCC

time slots. If only one power measure is available, e.g. the power dissipated in the load 92 (Fig. 8), there has to be some inherent knowledge of the expected power to the transmitter device. In Fig. 13, a PVT (Power Versus Time) mask for 8PSK modulated normal burst is shown. The PVT mask defines the envelope range in which the 8PSK signal is allowed to vary. At a short time period before 200 and after 202 the main signal period 204, the maximum and minimum power curves are separated by only 2.4 dB. This implies that without any knowledge of the actual system, the actual power of the signal is known with an accuracy of at least 2.4 dB. However, in most cases, design considerations are known and the accuracy of the power is generally much higher, in a typical case 0.3-0.5 dB. By performing an output power measurement during at least one of these periods a calibration of the phase shift can be achieved, even though the main signal is of a non-constant envelope type. The power level in this period has a known relationship to the average power over the entire burst. If the phase error of the amplifier varies with output power, the maximum value for the envelope will be in phase, while the phase shift at dips in the envelope may differ. The phase shift monitoring is thus performed during a period of transmission of a constant amplitude signal within a non-constant envelope signal.

If any correction of the phase shift is needed, a correction of the phase shift added to the first signal is preferably performed when no useful signal is transmitted from the transceiver device, e.g. during a guard period between two time slots. Since the guard time is long enough to perform all setting procedures for the new phase shift, this will ensure that the signal transmitted during the following time slot does not have any defects caused by a phase-shifting in progress.

In the above case, an a-priori knowledge about the expected amplitude of the transmitted signal is required. However, in more general transmission situations, such knowledge is not always available. In Fig. 14, another embodiment of a power meter 93 is illustrated. Only parts, which are directly involved, are illustrated. In this embodiment, the power meter 93 is still

connected to measure the power over the load 92. However, the power meter 93 now also is supplied with signals from a first and a second power sensor 96, 97, measuring the output power from the power amplifiers 64 and 84, respectively. In this manner, the power meter can keep track on the power entering the hybrid combiner and the power exiting from it. A signal corresponding to

$$1 - \frac{P_L}{(P_{TX1} + P_{TX2})}$$

where P_L is the power dissipated over the load and P_{TX1} and P_{TX2} are the powers of the amplifier outputs. This quantity corresponds to the cosine factor between the signals from the power amplifiers. The phase shifter 63 (Fig. 6) can then according to this adjust any phase shift, if necessary. Such an arrangement may be very useful, e.g. if downlink power control is applied.

By measuring also the power of the components, it becomes possible to perform calibration of the phase shift also during periods in which non-constant envelope signals are transmitted. However, performing it during an arbitrary signal section induce a lot of problems. One solution is, however, to use signal sections of a-priori known digital content. When transmitting a time slot of data using e.g. GMSK modulation or 8PSK modulation, a section of "training symbols" is included in the data. This is schematically illustrated in Fig. 15. These training symbols are well-known and an expected output signal can easily be calculated. By monitoring power values according to Fig. 14, during the transmission of such training symbols, an actual output signal can be compared with the expected one, and a phase difference can be detected and used for calibration purposes.

Above, one embodiment of shifting the phase of a signal is illustrated. However, anyone skilled in the art understands that also other phase-shifting devices and methods can be employed in order to achieve the functions of the present invention. When operating in a TCC mode, one

attractive alternative arises. Fig. 16 illustrates some selected parts of a transmitter arrangement having an alternative phase-shifting arrangement. The power meter 93 is as before connected to a phase shifter 63. However, in this embodiment, the phase shifter 63 is directly connected to the GMSK modulation means in the DSP 52. The phase shifter 63 evaluates the power signals from the power meter 93 and provides a requested phase shift $\Delta\theta$ to the GMSK modulation means 54. The GMSK modulation means 54 uses typically a tabulated state machine 98 operating according to a transfer function between the phase shift induced by the digital signal and time. A graph of such a function is illustrated in Fig. 17. The transfer function is drawn with a full line and denoted by 210. By simply adding the phase shift $\Delta\theta$ provided by the phase shifter 63 to the value achieved from the transfer function, the entire signal will be provided with an additional phase shift. The phase-shift compensated transfer function will then look like the broken line 212.

In the above embodiments, the DSP's 52, 72 have comprised one constant-envelope modulation means and one non-constant envelope modulation means, in the form of a GMSK modulator and an 8PSK modulator. The DSP's may also comprise different types and different number of modulators. Other types of phase shift keying, such as 4PSK, are examples of possible other non-constant envelope modulators.

Another interesting non-constant envelope modulator that can be used in the present invention is a modulator for combined carrier signals. One embodiment of such a multi-carrier modulator is illustrated in Fig. 20. Here, two carriers of a GMSK modulation are combined, but it is also possible to combine carriers of other modulation schemes, e.g. 8PSK. Also, it is possible to combine carriers having different modulation schemes, e.g. one GMSK and one 8PSK carrier. Moreover, the basic ideas of this carrier combining can be generalized into more than two carriers. However, in such cases, bandwidth restrictions may set a practical limit.

The DSP 52 comprises a carrier combiner modulation means 220, in the present embodiment in turn comprising two GMSK modulators 54A, 54B. One of the outputs of the selector 55 is connected to the first GMSK modulator 54A. The first GMSK modulator means 54A is thus provided with the digital signal provided by the input 51, which represents the signal intended for the first carrier. An additional digital signal input 228 is provided to the second GMSK modulator 54B, whereby this modulator is provided with a digital signal, which represents the signal intended for the second carrier. An additional information input 222 is provided, which carries data defining a frequency difference between the two carriers, or in the present embodiment half this frequency difference. The digital signals are GMSK-modulated separately into digital I and Q representations. The I and Q representation from the first GMSK modulator 54A is then modulated in a pre-modulator 225 with a signal having half the difference frequency provided by input 222, but with an opposite phase direction, i.e. in practice minus half the difference frequency. The I and Q representation from the second GMSK modulator 54B is similarly modulated in a pre-modulator 226 with a signal having half the difference frequency provided by input 222. The digital I and Q signals are finally added in a summing means 224, providing a signal representing two digital signals on one carrier each, pre-modulated to frequencies of $\pm\Delta f/2$. The up-conversion of the frequency, taking place later in the chain, the frequency is selected to be the mean frequency of the two carriers.

The digital signals resulting directly from the GMSK modulators 54A, 54B are constant envelope signals. However, after the pre-modulation by the difference frequency, they exhibit a non-constant envelope behavior. The complex sum of these two signals is also of a non-constant envelope character. In analogy with the 8PSK case described above, it is possible to separate this sum signal into two components 31, 32 with constant envelopes (cf. Fig. 4). The process then continues in analogy with the 8PSK case described further above.

By using this scheme, any arbitrary combination of modulation schemes in any number of carriers can be combined and processed as a non-constant envelope signal. Since the choice of modulation schemes furthermore can be performed on a time slot basis, this opens up for a very high flexibility in the use of the transceiver unit arrangement according to the present invention. However, there are also some drawbacks present. First of all, since the frequency difference between the carriers is modulated into the signal even before the separation into components, the bandwidth of the signals that has to be treated throughout the transceiver unit path is increased. The increase in bandwidth corresponds approximately to the frequency difference. This puts very high demands on the components in the transceiver unit, in particular on the DAC's. There are, however, already today DAC components that would be able to handle at least neighboring frequencies. Using more than two carriers will of course make the bandwidth requirements even larger.

Another problem is that, if using more than two carriers, the output power per carrier will decrease. Since the total power is restricted by the sum of the power of each individual transceiver unit, this maximum power can not be exceeded. When having three or more carriers, the sum signal has to be scaled down in order to assure that it can be separated into components, i.e. it has to be kept within double the component amplitude. In order to be absolutely sure that every possible combination will be covered, the output power of each carrier will be reduced by a factor $n/2$, where n is the number of carriers.

The principle of separating a non-constant envelope signal into constant envelope components opens up for a very flexible use of the transceiver units. However, this principle is not very power efficient when handling signals of low amplitude. Even if the total signal has a low amplitude, the components have high amplitudes, which means that a large portion of the power will be wasted when re-combining the components in the hybrid combiner. A large power will dissipate through the load.

Also, when the total signal has a low amplitude, small changes in the signal may cause very large phase changes of the components. The bandwidth necessary to process the components will therefore be larger when the total signal has a low amplitude.

A way to reduce the problems described above is to renounce the demand of keeping the component amplitude constant. By letting the component amplitude decrease when the total signal amplitude becomes small, some advantages are achieved. The required bandwidth will decrease and the total power efficiency will increase. However, since the present invention is intended to operate also with amplifiers not being perfectly linear, such component amplitude variations have to be kept within certain limits.

Another aspect to consider when deciding the reduction of the component amplitudes is the efficiency of the power amplifiers. Most power amplifiers exhibit the highest efficiency at the highest output values. A too large reduction in component amplitude will indeed result in higher efficiency in the combiner stage, but may reduce the efficiency in the power amplifier even more. The component amplitude reduction is thus preferably performed to optimize the allover efficiency.

As described in the above embodiments, there are a number of interesting advantages arising by using the present invention. One of the main advantages is the high flexibility in using the arrangement. A user may easily, even on a time slot basis, change between different transmitting configurations. It is thus possible to change e.g. between high capacity and high output power, depending on the actual need. No re-calibrations have to be performed and the changes typically involve solely software changes.

It will be understood by those skilled in the art that various modifications and changes may be made to the present invention without departure from the scope thereof, which is defined by the appended claims.

REFERENCES

S. Mann, M. Beach, P. Warr and J. McGeehan, "Increasing the talk-time of mobile radios with efficient linear transmitter architectures", Electronics & Communication Engineering Journal, April 2001, Vol. 13, No. 2, pp. 65-76.

US patent 5,990,734.

CLAIMS

1. Transmitter arrangement, comprising:

a first modulation unit (50) having a first digital signal processor (52) and a first analogue signal generator (56);

said first digital signal processor (52) having a first digital signal input (51);

a first power amplifier (64), connected to an output of said first analogue signal generator (56);

a second modulation unit (70) having a second digital signal processor (72) and a second analogue signal generator (76);

said second digital signal processor (72) having a second digital signal input (71);

a second power amplifier (84), connected to an output of said second analogue signal generator (76);

combiner device (90) connected to outputs of said first and second power amplifiers (64, 84); and

transmitter device (91) connected to an output of said combiner device (90),

characterized in that said first digital signal processor (52) further comprises:

at least one first non-constant envelope modulation means (53);

a first signal component separator (65) connected to an output of said at least one first non-constant envelope modulation means (53);

a first output of said first signal component separator (65) being connectable to said first analogue signal generator (56);

first means for receiving modulation instructions (49);

at least one first constant envelope modulation means (54) connectable to said first analogue signal generator (56); and

first modulation selecting means (55) for connecting a modulation means to said first digital signal input (51) in response to received modulation instructions (49).

2. Transmitter arrangement according to claim 1, **characterized in that** said second digital signal processor (72) further comprises:

at least one second non-constant envelope modulation means (73) of the same type as said at least one first non-constant envelope modulation means (53); and

a second signal component separator (85) connected to an output of said at least one second non-constant envelope modulation means (73);

an output of said second signal component separator (85) being connectable to said second analogue signal generator (76);

a sum of a signal of said first output of said first signal component separator (65) and a signal of said output of said second signal component separator (85) being equal to a signal of said output of said at least one first non-constant envelope modulation means (53).

3. Transmitter arrangement according to claim 1, **characterized in that** a second output of said first signal component separator (65) being connectable to said second analogue signal generator (76).

4. Transmitter arrangement according to claim 1, 2 or 3, **characterized in that**

said second digital signal processor (72) further comprises:

second means for receiving modulation instructions (69);

at least one second constant envelope modulation means (74) connectable to said second analogue signal generator (76); and

second modulation selecting means (75) for connecting a modulation means to said second digital signal input (71) in response to received modulation instructions (69).

5. Transmitter arrangement according to claim 4, **characterized in that** said first and second modulation selecting means (55, 75) are operable on a time slot basis.

6. Transmitter arrangement according to any of the claims 1 to 5, **characterized by** further comprising:

first power monitor (93) sensing a total power to said transmitter device (91) or a quantity directly related thereto; and

phase-shifter (63) connected to said first power monitor (93), arranged for causing a phase shift of an analogue signal generated by said first analogue signal generator (56) in response to said sensed total power.

7. Transmitter arrangement according to claim 6, **characterized in that** said first power monitor (93) is a power meter of a load (92) of said combiner device (90).

8. Transmitter arrangement according to claim 6 or 7, **characterized in that** said phase-shifter (63) comprises means for complex multiplication of said phase shift ($\Delta\theta$) with a digital signal to be inputted to said analogue signal generator (56).

9. Transmitter arrangement according to claim 6 or 7, using GMSK modulation, **characterized in that** said phase-shifter (63) comprises means for introducing a phase offset ($\Delta\theta$) in said GMSK modulation, generated by using a table driven state machine in said first digital signal processor (52).

10. Transmitter arrangement according to any of the claims 6 to 9, **characterized by** means for providing said first and second digital inputs (51, 71) with the same digital signal, and said first and second means for receiving instructions (49, 69) with the same instructions of a constant envelope modulation, allowing transmitter coherent combining.

11. Transmitter arrangement according to any of the claims 6 to 10, **characterized by** further comprising:

second power monitor (96) sensing a power on said output of said first power amplifier (64) and being connected to said phase-shifter (63); and

third power monitor (97) sensing a power on said output of said second power amplifier (84) and being connected to said phase-shifter (63);

said phase-shifter (63) being arranged for causing a phase shift ($\Delta\theta$) in response to a comparison between said sensed total power and said sensed power on said output of said first and second power amplifier (64, 84), respectively.

12. Transmitter arrangement according to any of the claims 1 to 11, **characterized in that** said first and second non-constant envelope modulation means are selected from the list of:

4-PSK modulation means;

8-PSK modulation means (53, 73); and

means (220) for combination of at least two carriers.

13. Transmitter arrangement according to any of the claims 4 to 12, **characterized in that** said first and second constant envelope modulation means are GMSK modulation means (54, 74).

14. Method for generating a transmitter signal in a transmitter arrangement (45) having at least a first and a second modulation unit (50, 70) arranged in parallel, each one allowing for at least one non-constant envelope modulation and at least one constant envelope modulation, said first modulation unit (50) having a first analogue signal generator (56), said second modulation unit (70) having a second analogue signal generator (76), comprising the steps of:

providing digital signal (51, 71) to said first and second modulation units (50, 70);

providing modulation information (49, 69) to said first and second modulation units (50, 70);

creating a first input signal to said first analogue signal generator (56) by performing a constant envelope modulation of a first digital signal (51) provided to said first modulation unit (50) as a response of said modulation information (49) being a request for a constant envelope

modulation, and by performing a non-constant envelope modulation of said first digital signal (51) and separating a first component of said non-constant envelope modulated first digital signal as a response of said modulation information (49) being a request for a non-constant envelope modulation;

creating a second input signal to said second analogue signal generator (76) by performing a constant envelope modulation of a second digital signal (71) provided to said second modulation unit (70) as a response of said modulation information (69) being a request for a constant envelope modulation, and by performing a non-constant envelope modulation of said first digital signal (51) and separating a second component of said non-constant envelope modulated first digital signal as a response of said modulation information (69) being a request for a non-constant envelope modulation;

generating a first output signal in said first analogue signal generator (56) according to said first input signal;

generating a second output signal in said second analogue signal generator (76) according to said second input signal;

amplifying said first output signal;

amplifying said second output signal;

combining said first and second amplified output signals to form an analogue transmitter signal.

15. Method according to claim 14, **characterized in that** said providing steps are performed on a time slot basis.

16. Method according to claim 14 or 15, **characterized in that** said modulation information comprises a request for a non-constant envelope modulation, whereby said step of creating a second input signal to said second analogue signal generator (76) is performed on said first signal (51) in said first modulation unit (50), said method comprising the further step of transferring of said second input signal from said first modulation unit (50) to said second analogue signal generator (76).

17. Method according to claim 14 or 15, **characterized in that** said modulation information comprises a request for a non-constant envelope modulation, and said second digital signal (71) is identical with said first digital signal (51), whereby said step of creating a second input signal to said second analogue signal generator (76) is performed on said second signal (71) in said second modulation unit (70).

18. Method according to claim 16 or 17, **characterized in that** said non-constant envelope modulation is a 8-PSK modulation (53, 73).

19. Method according to claim 16 or 17, **characterized in that** said non-constant envelope modulation is a multiple-carrier GMSK modulation (220), whereby said method comprises the steps of providing a set of at least two digital signals to both said first and said second modulating units, whereby said creating steps comprise the steps of performing a GMSK modulation of each digital signal and digital combining said modulated signals to form a non-constant envelope multi-carrier signal, whereby said separating step is performed on said non-constant envelope multi-carrier signal.

20. Method according to claim 14 or 15, **characterized in that** said modulation information comprises a request for transmitter coherent combining of a constant envelope modulation signal, and said first digital signal (51) is identical with said second digital signal (71).

21. Method according to any of the claims 16 to 20, **characterized by** the further steps of:

monitoring a power of said analogue transmitter signal or a quantity directly related thereto; and

shifting a phase of said first output signal according to said power.

22. Method according to claim 21, **characterized in that** said monitoring step comprises the step of measuring a power rejected during said

combining step, whereby said power of said analogue transmitter signal is provided as a complementary quantity.

23. Method according to claim 21 or 22, **characterized in that** said shifting step in turn comprises the step of adjusting an initial offset phase ($\Delta\theta$) of said first or second modulating in a guard period between two time slots.

24. Method according to claim 21 or 22, **characterized in that** said shifting step in turn comprises the step of adding a phase shift ($\Delta\theta$) in connection to the generation of the first output signal.

25. Method according to any of the claims 16 to 24, **characterized in that** said monitoring and phase shifting is performed when a constant envelope modulation with transmitter coherent combining is used, whereby said phase shifting is preserved when selecting a non-constant envelope modulation.

26. Method according to any of the claims 16 to 24, **characterized in that** said monitoring and phase shifting is performed during transmission of a constant amplitude period of a non-constant envelope signal.

27. Method according to any of the claims 16 to 26, **characterized by** the further step of measuring instantaneous power of said first and second analogue output signals, whereby said shifting is performed according to a comparison of said power of said analogue transmitter signal and said power of said first and second analogue output signals.

28. Method according to claim 27, **characterized in that** said shifting in the case of transmitter coherent combining is performed according to:

$$\phi_{shift} = \cos^{-1}(P_{TR} / (P_{TX1} + P_{TX2})),$$

where P_{TR} is said total power and P_{TX1} and P_{TX2} are said power of said first and second analogue output signals, respectively.

29. Method according to claim 27, **characterized in that** said comparison is performed during a period of a known training sequence in a time slot.

5 30. Method according to any of the claims 14 to 29, **characterized by** the further steps of:

reducing envelopes of said first and second signals when said modulated signal has a low amplitude.

10 31. Method according to claim 30, **characterized in that** said step of reducing envelopes comprises minimizing of power consumption.

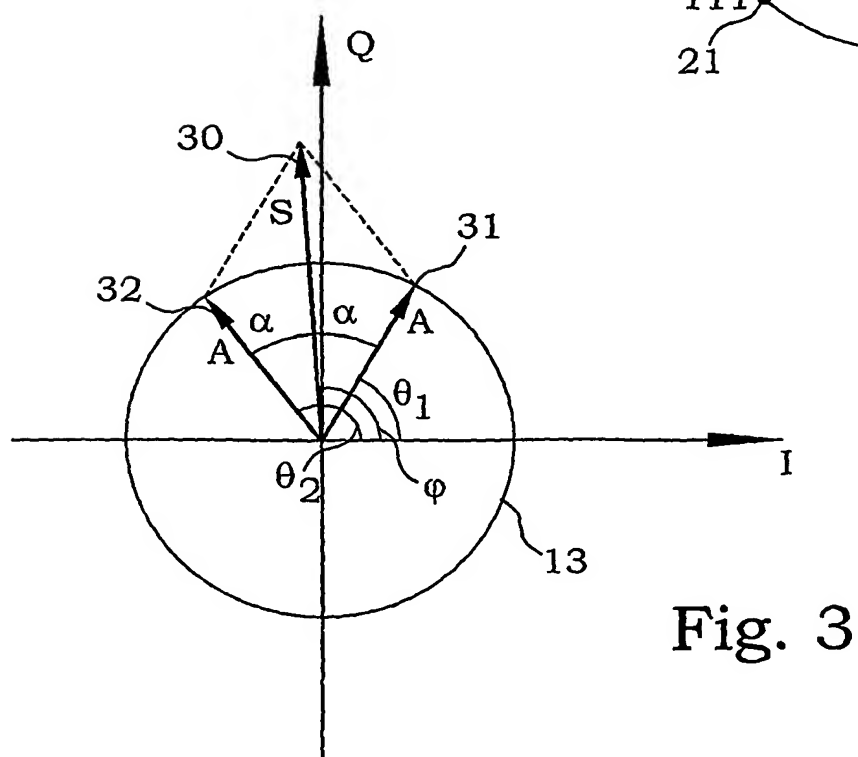
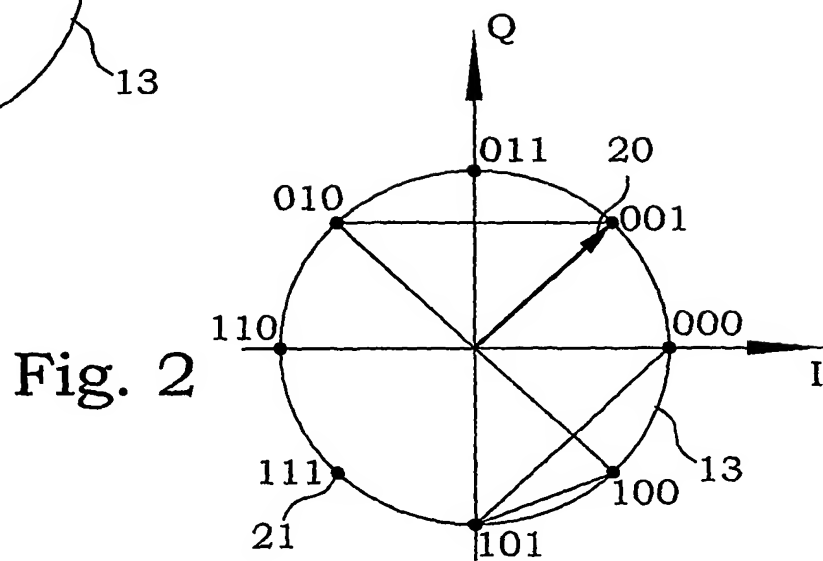
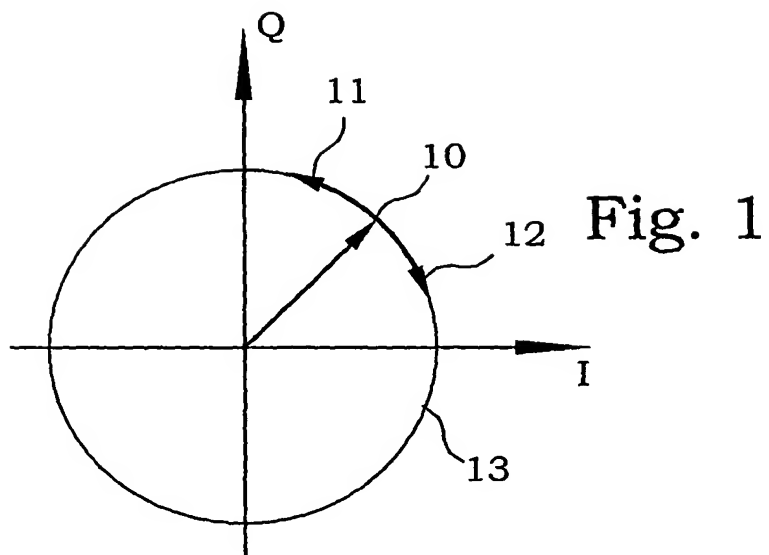
15 32. Method according to any of the claims 14 to 31, **characterized by** the further step of:

storing an adjusted phase shift value for each one of a set of used frequencies.

20 33. Method according to claim 32, **characterized by** the further step of:

storing an adjusted phase shift value for each one of a set of used frequency generators (61A, 61B) for each of said used frequencies.

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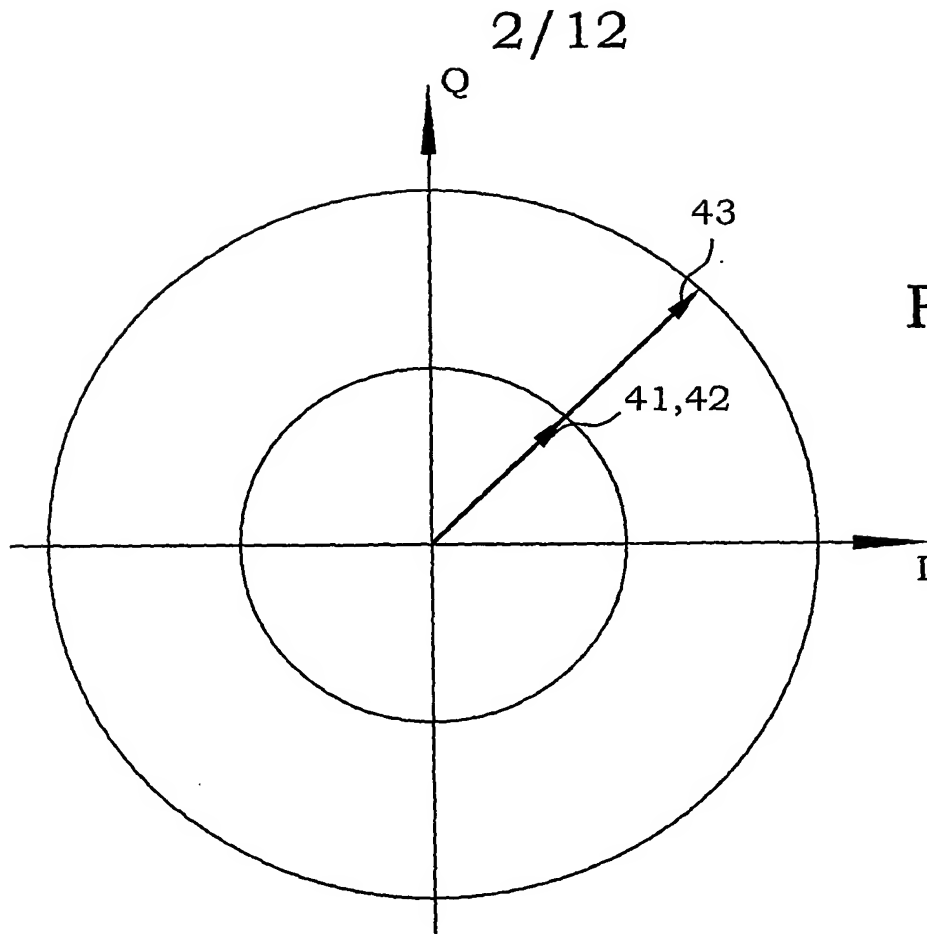


Fig. 4

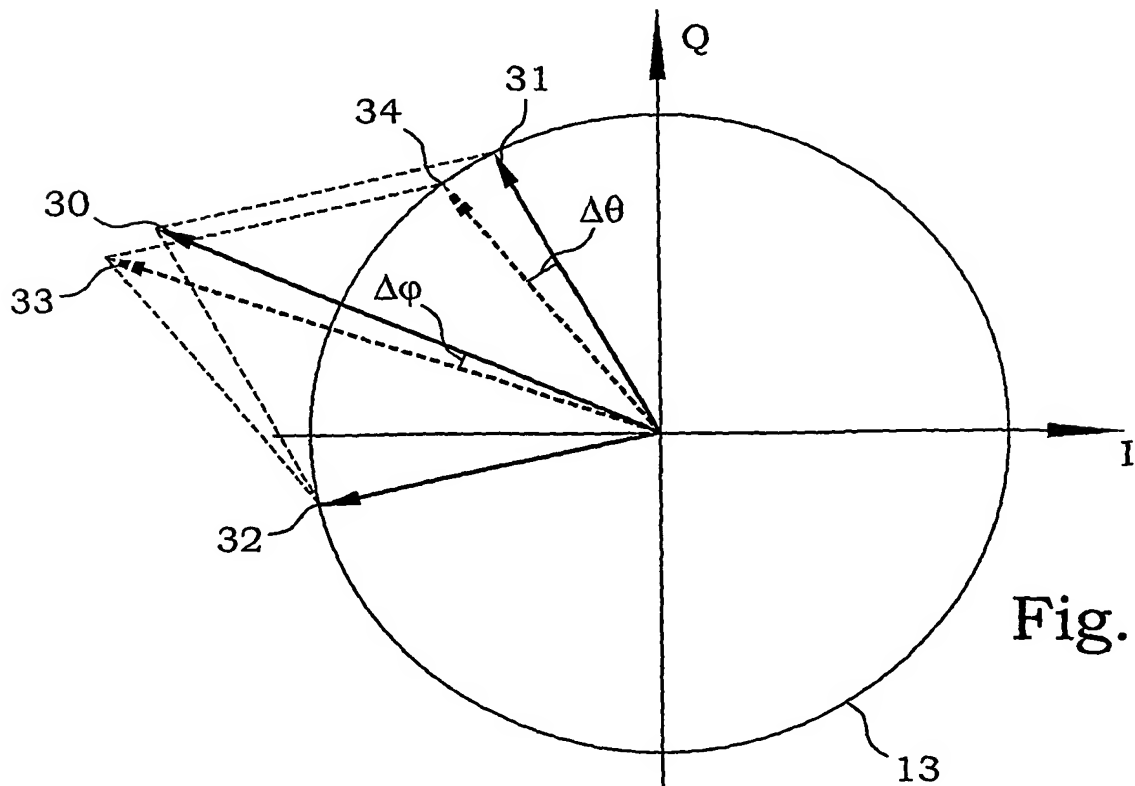


Fig. 5

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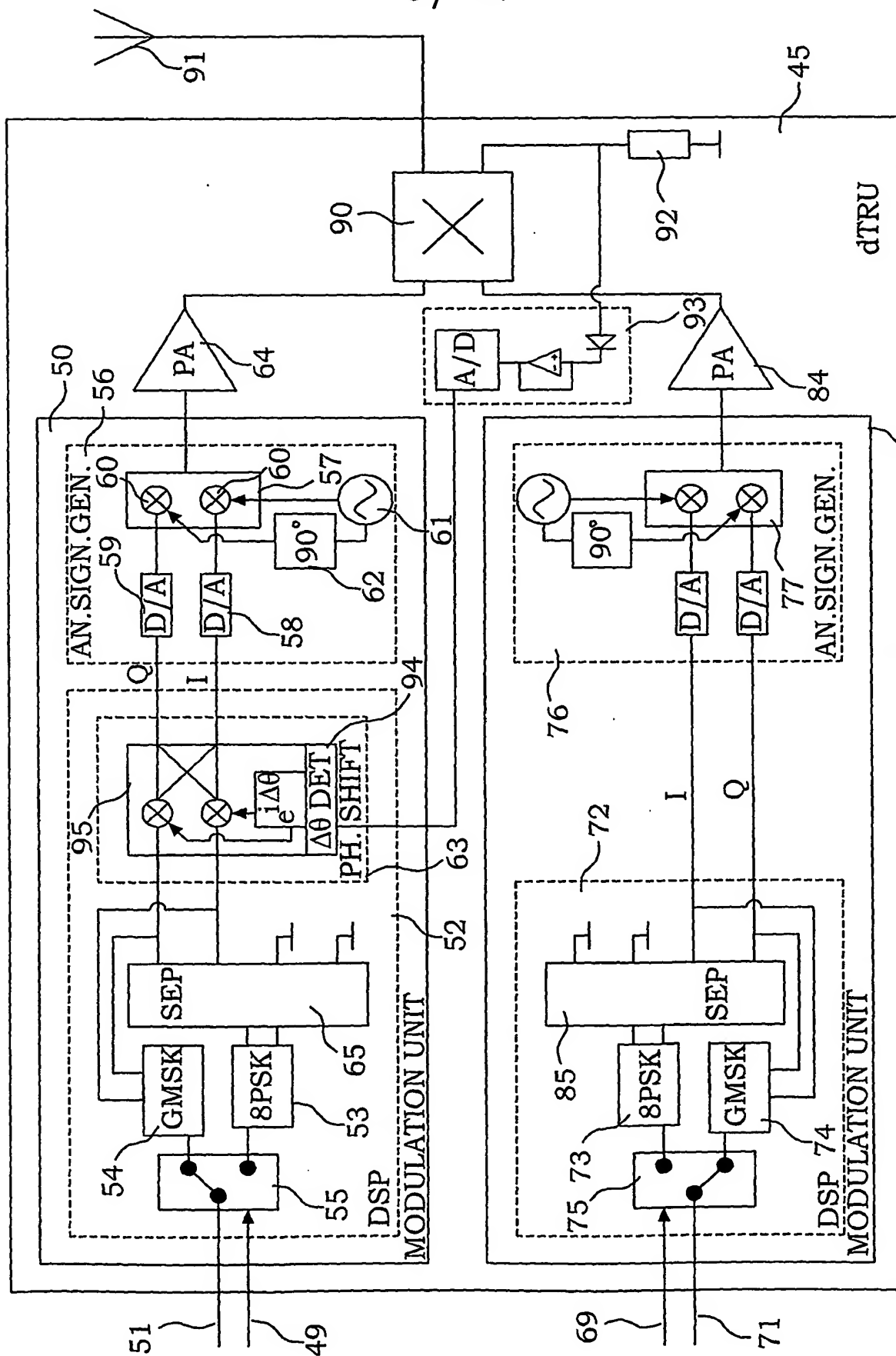


Fig. 6

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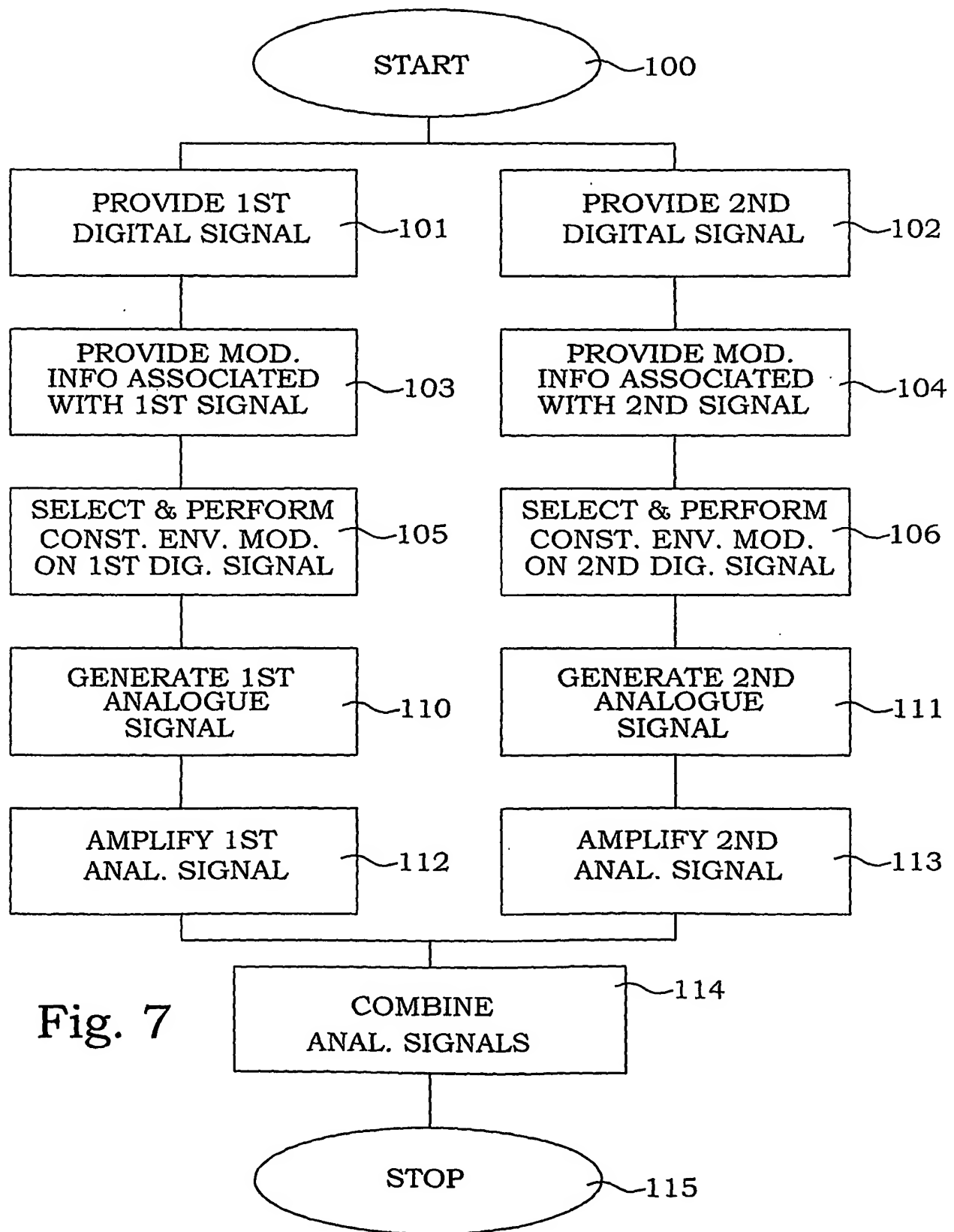


Fig. 7

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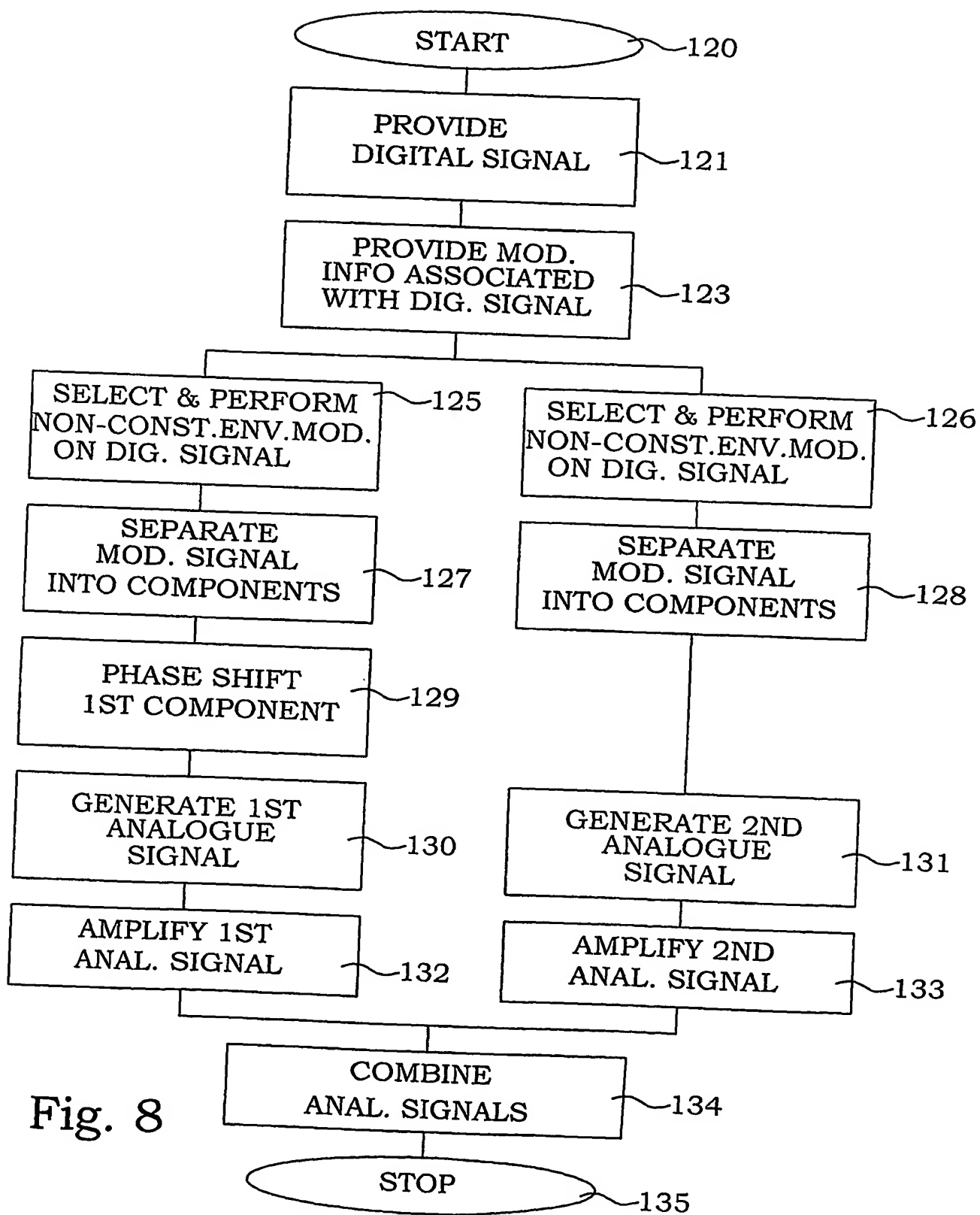


Fig. 8

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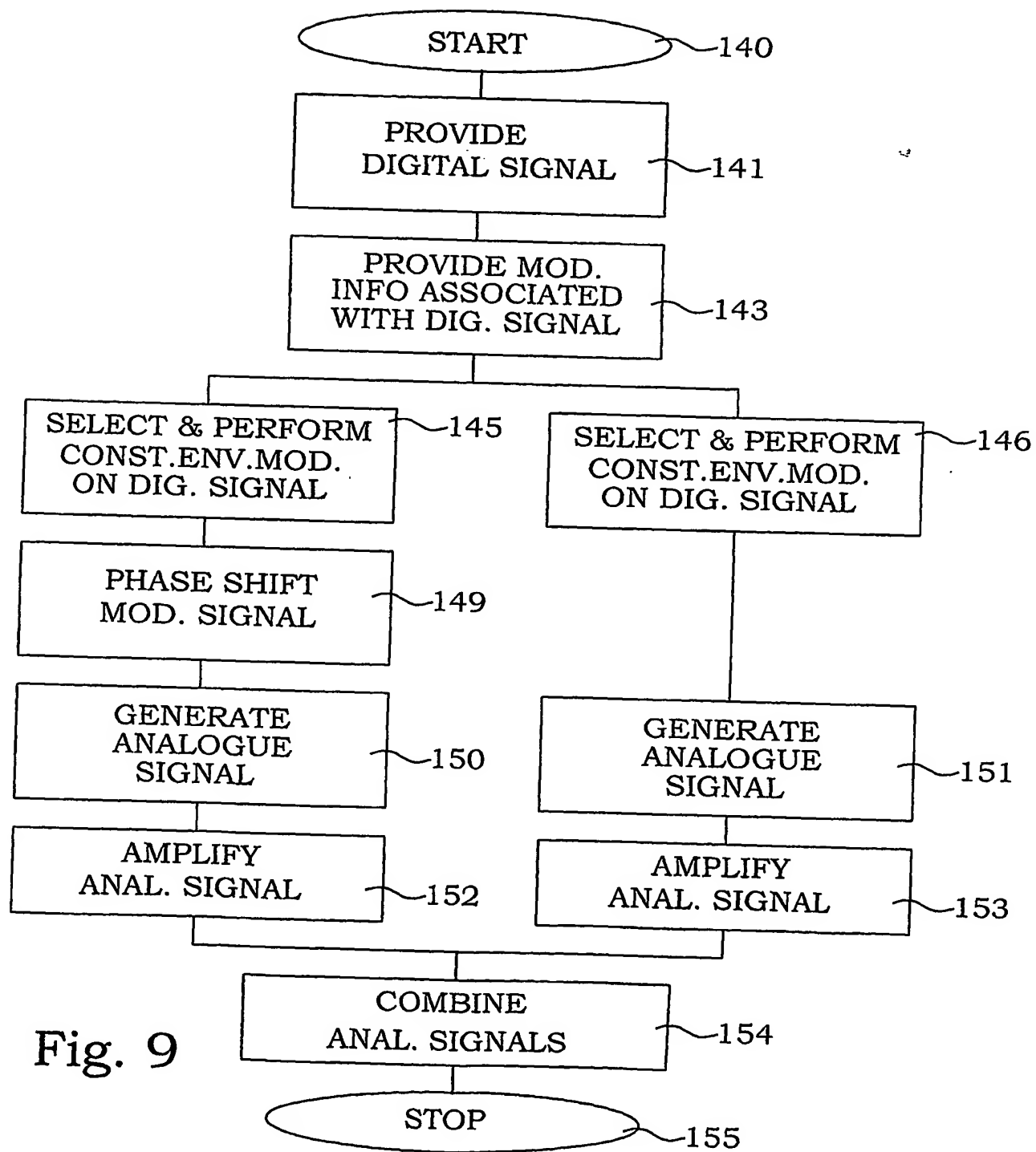


Fig. 9

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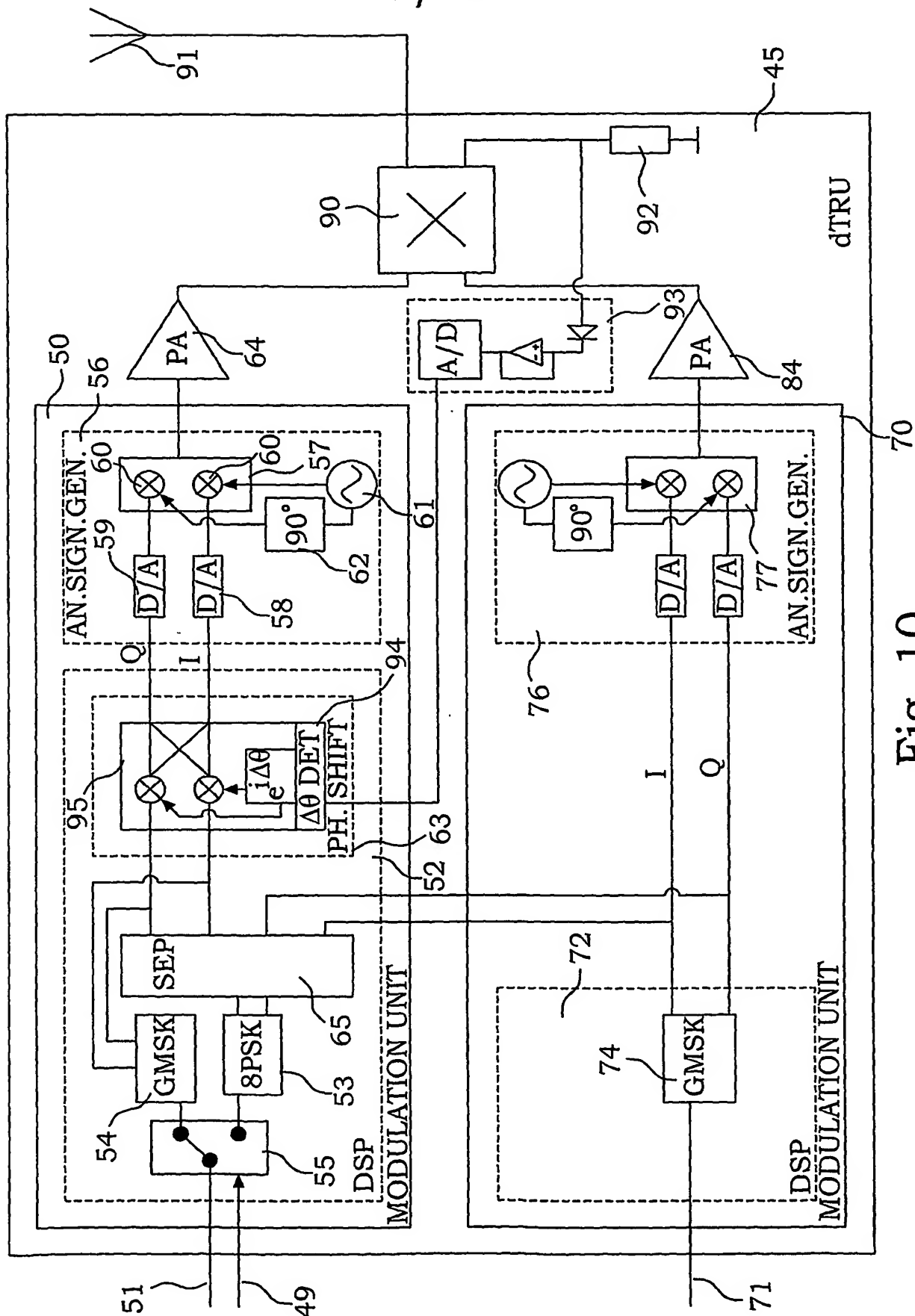


Fig. 10

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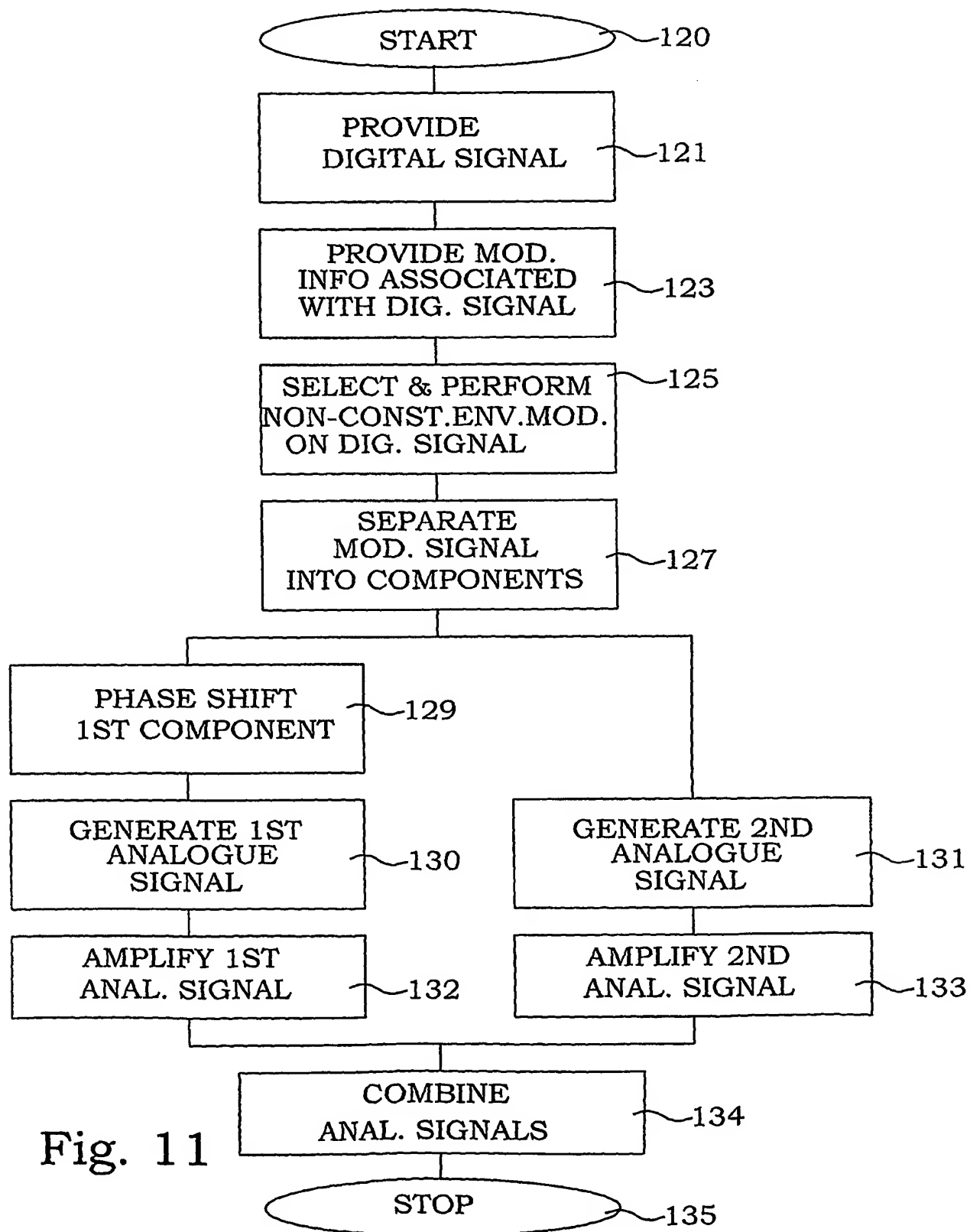


Fig. 11

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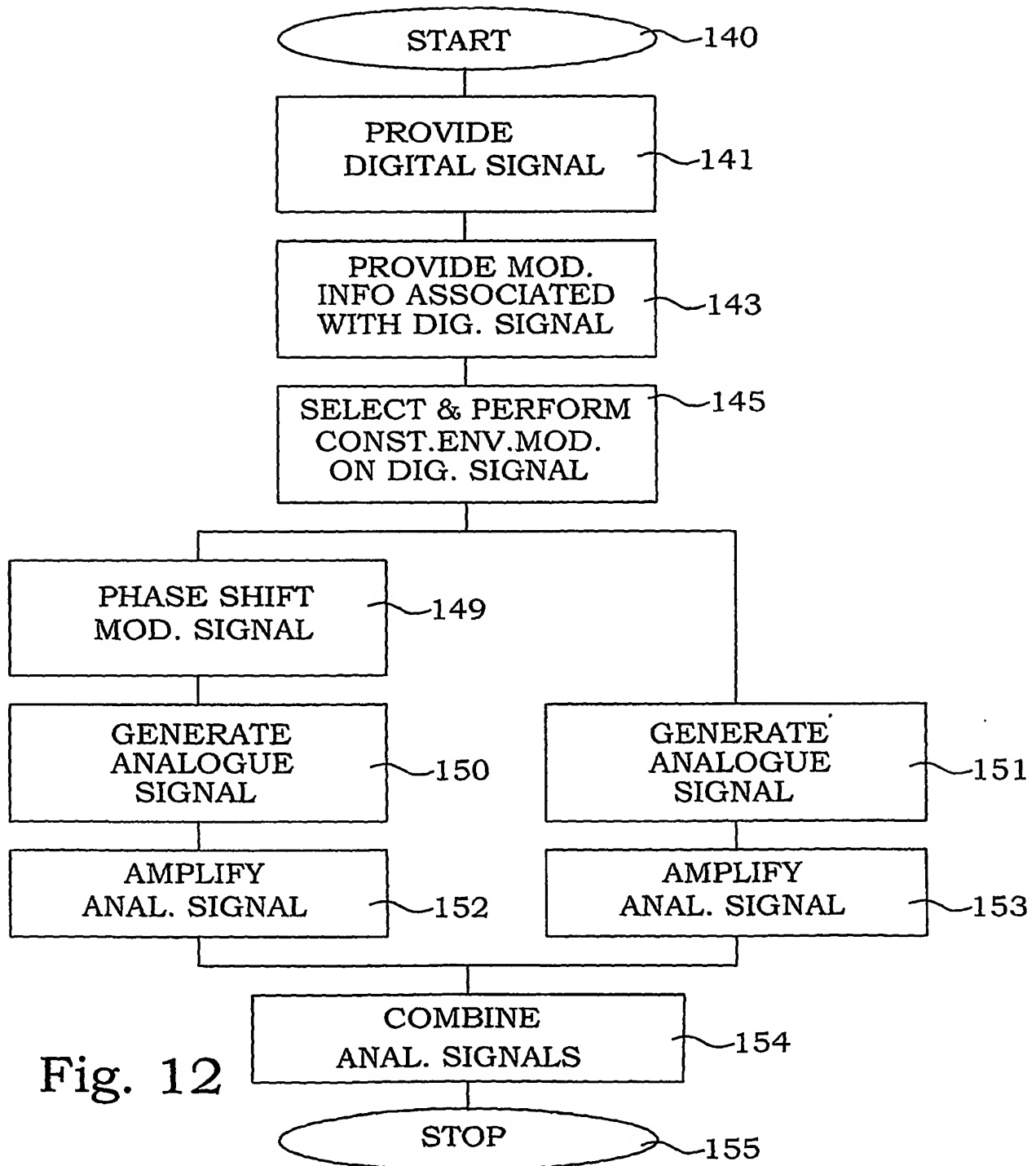


Fig. 12

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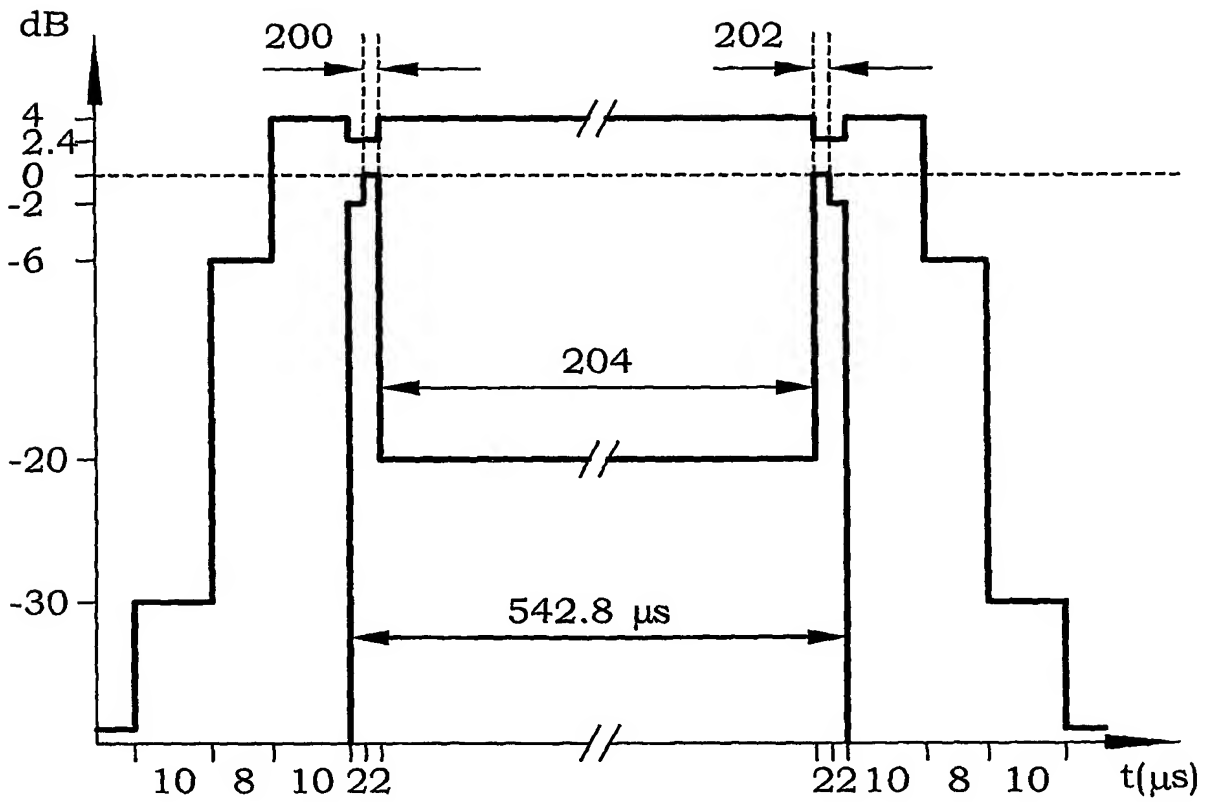
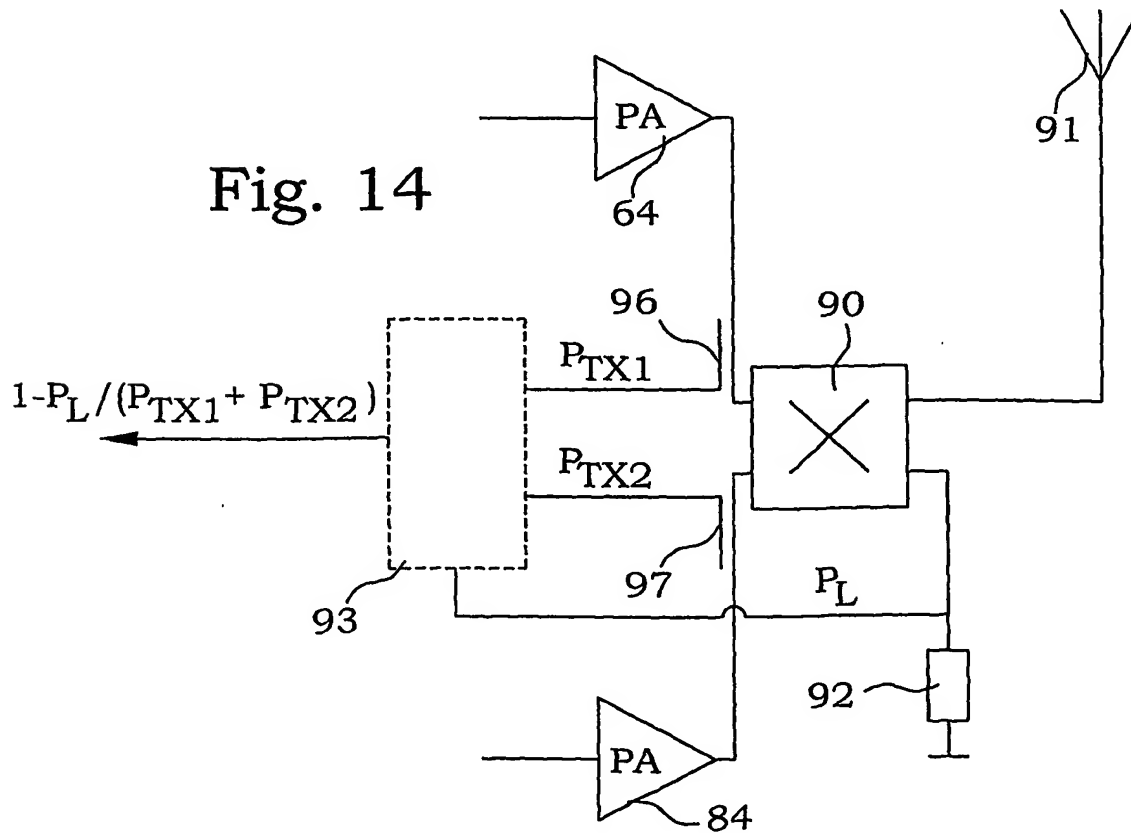


Fig. 13



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TIME SLOT

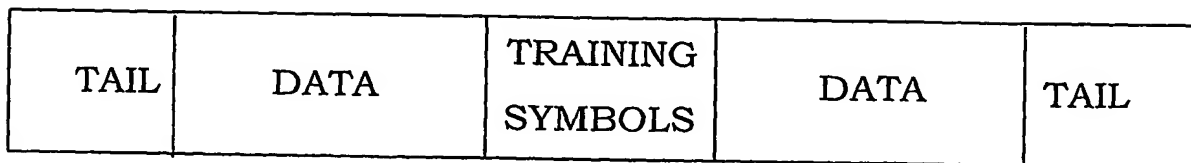


Fig. 15

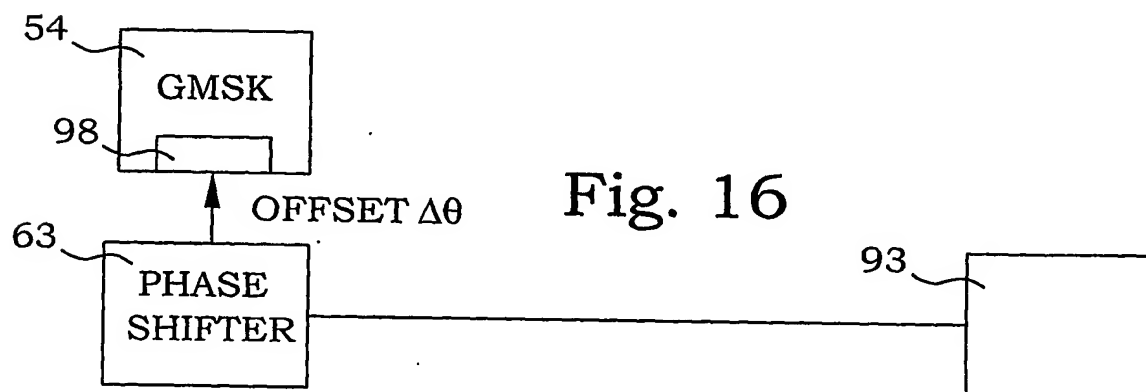


Fig. 16

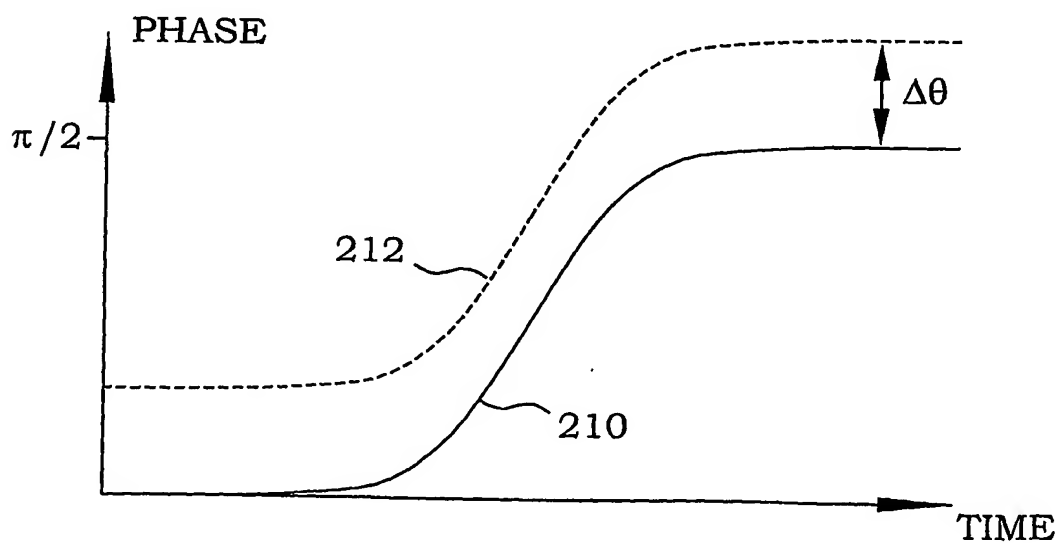


Fig. 17

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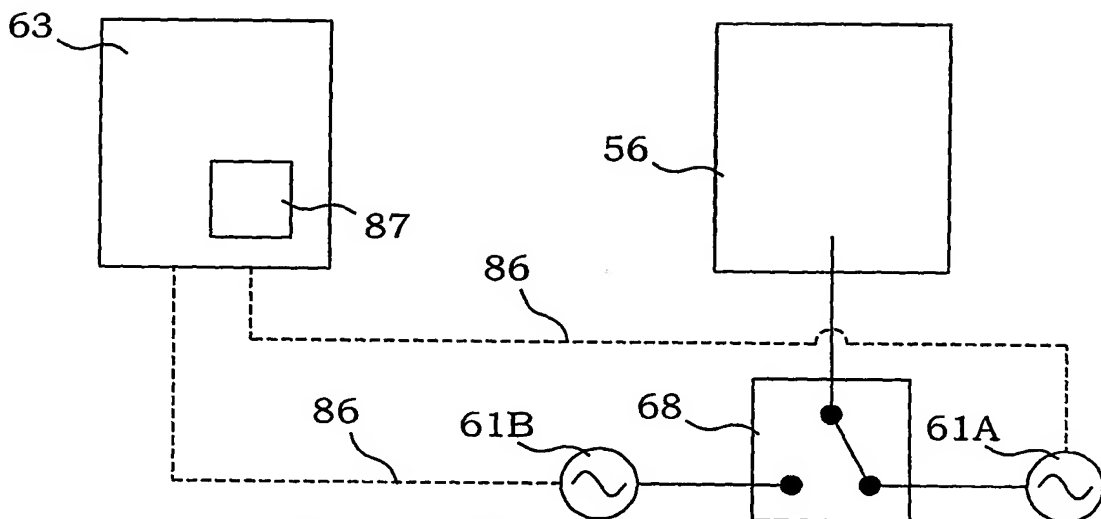


Fig. 18

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	A	B
f_1	$\Delta\theta_{A1}$	$\Delta\theta_{B1}$
f_2	$\Delta\theta_{A2}$	$\Delta\theta_{B2}$
f_3	$\Delta\theta_{A3}$	$\Delta\theta_{B3}$
f_4	$\Delta\theta_{A4}$	$\Delta\theta_{B4}$
	⋮	⋮

Fig. 19

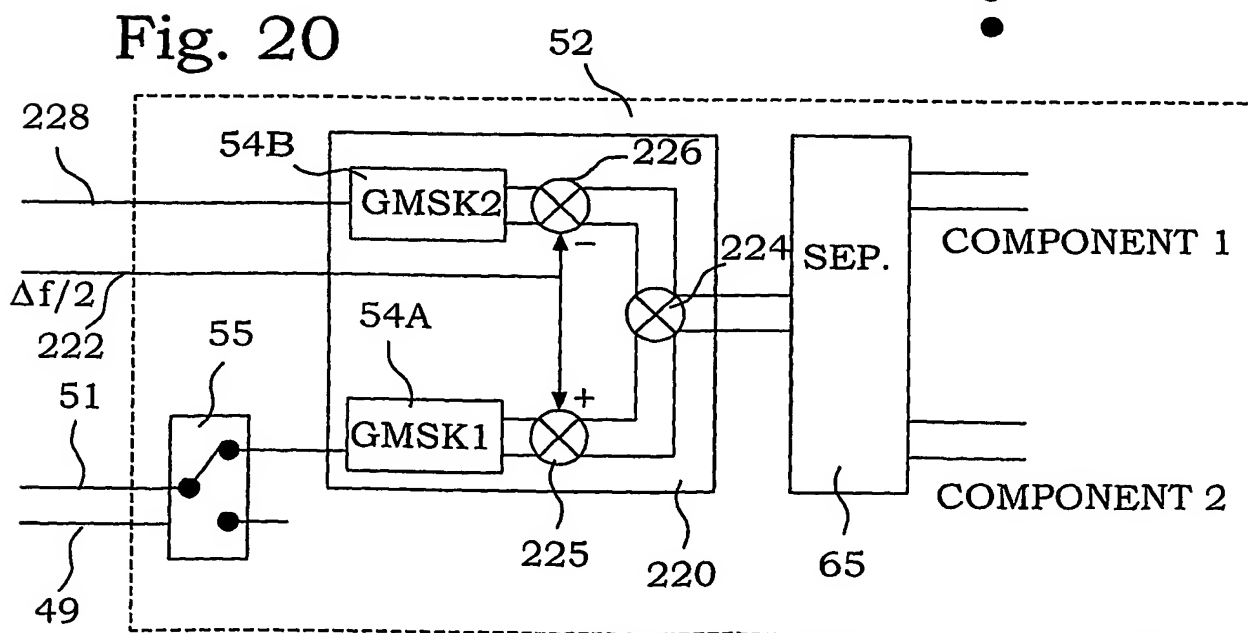


Fig. 20

INTERNATIONAL SEARCH REPORT

International application No.

PCT/SE 03/00459

A. CLASSIFICATION OF SUBJECT MATTER

IPC7: H04B 1/04, H03F 1/32

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC7: H04B, H03F, H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-INTERNAL, PAJ, INSPEC, WPI

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	ELECTRONICS & COMMUNICATION ENGINEERING JOURNAL, Volume, April 2001, S. Mann, M. Beach, P. Warr and J. McGeehan, "Increasing the talk-time of mobile radios with efficient linear transmitter architectures", page 70 - page 71, abstract --	1-33
A	US 5990734 A (ANDREW S. WRIGHT ET AL), 23 November 1999 (23.11.99), figures 2,9, abstract --	1-33
A	EP 1102392 A2 (INFINEON TECHNOLOGIES AG), 23 May 2001 (23.05.01), figure 1, abstract -- -----	1-33

☐ Further documents are listed in the continuation of Box C.☒ See patent family annex.

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"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

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"Y" document of particular relevance: the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

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Date of the actual completion of the international search

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INTERNATIONAL SEARCH REPORT
Information on patent family members

29/04/03

International application No.
PCT/SE 03/00459

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